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08/10/2005

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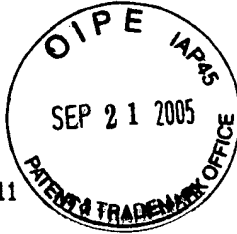
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/800,711	03/16/2004	Akio Hirata	60188-802	1762

TITLE OF INVENTION: SEMICONDUCTOR MEMORY AND SEMICONDUCTOR INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	11/10/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
DINH, SON T	2824	365-177000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. McDERMOTT WILL &

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3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.

OSAKA, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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